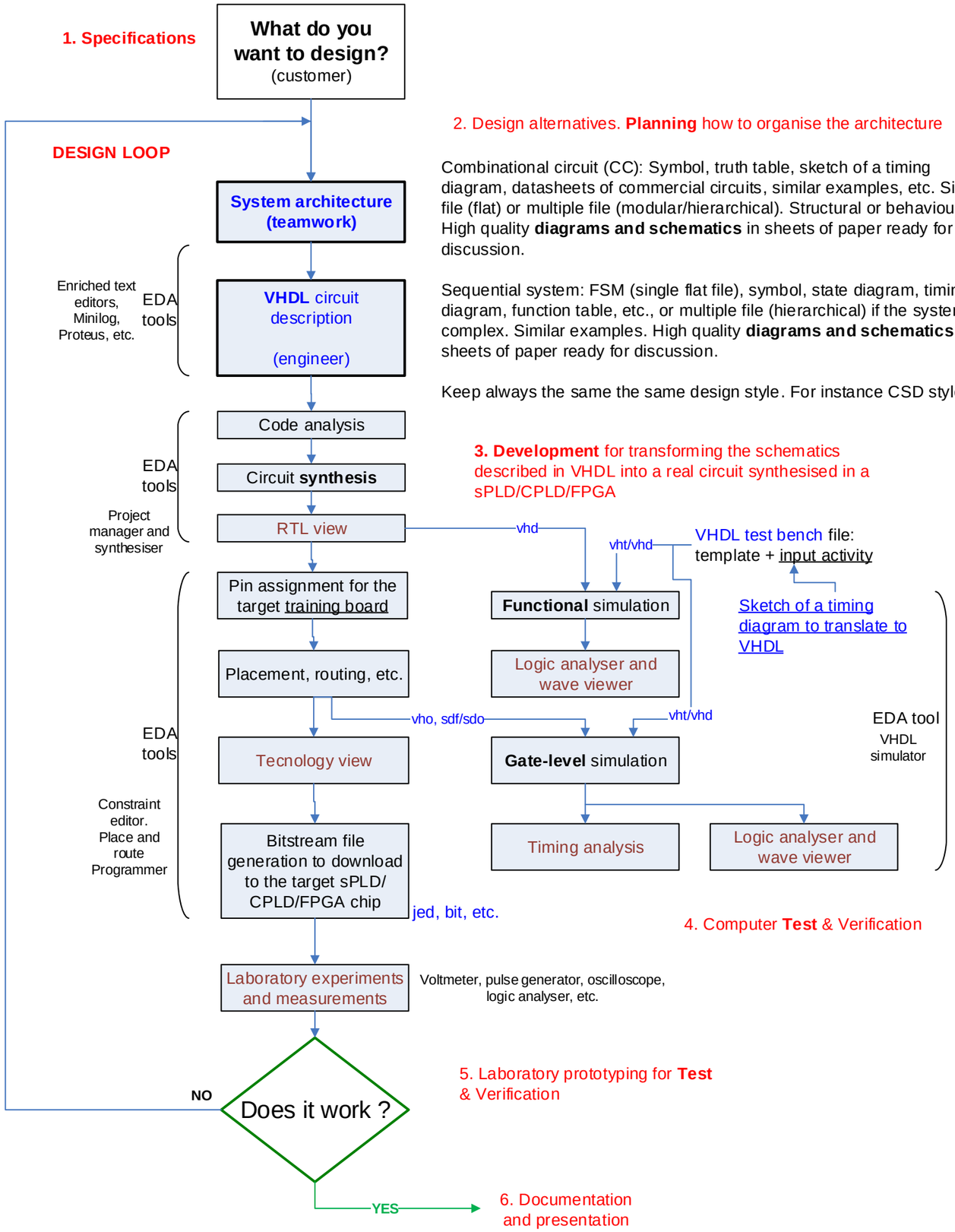

2. Design alternatives. **Planning** how to organise the architecture

Combinational circuit (CC): Symbol, truth table, sketch of a timing diagram, datasheets of commercial circuits, similar examples, etc. Single file (flat) or multiple file (modular/hierarchical). Structural or behavioural. High quality **diagrams and schematics** in sheets of paper ready for discussion.

Sequential system: FSM (single flat file), symbol, state diagram, timing diagram, function table, etc., or multiple file (hierarchical) if the system is complex. Similar examples. High quality **diagrams and schematics** in sheets of paper ready for discussion.

Keep always the same the same design style. For instance CSD style.