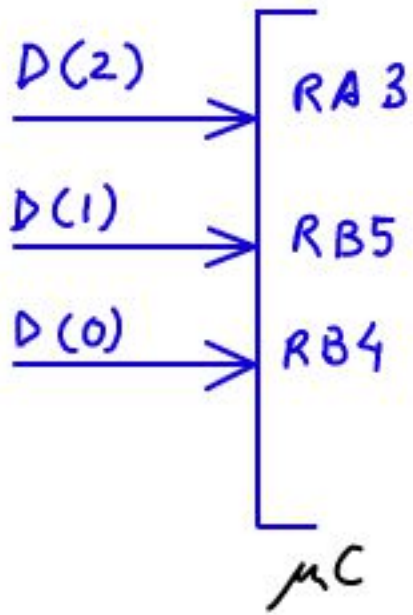
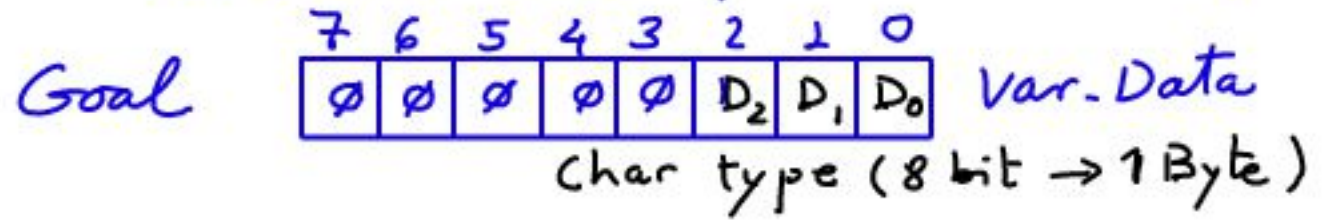


- Example on how to read/poll/acquire an input signal

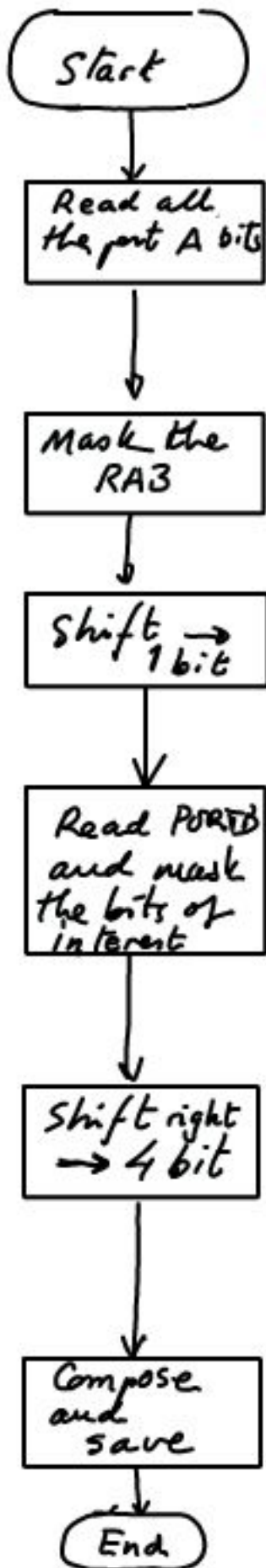


→ Let's capture in a RAM variable

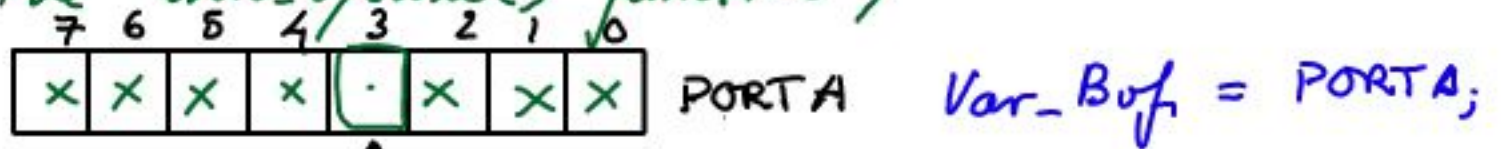
var_Data the input vector D(2..0)



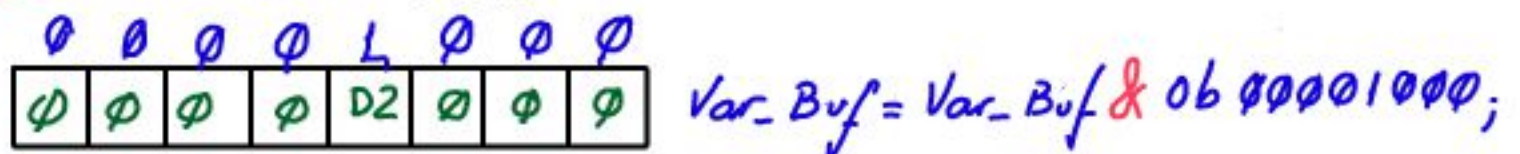
The plan is a sequence of operations using a convenient RAM buffer variable Var_Buf for saving partial results.



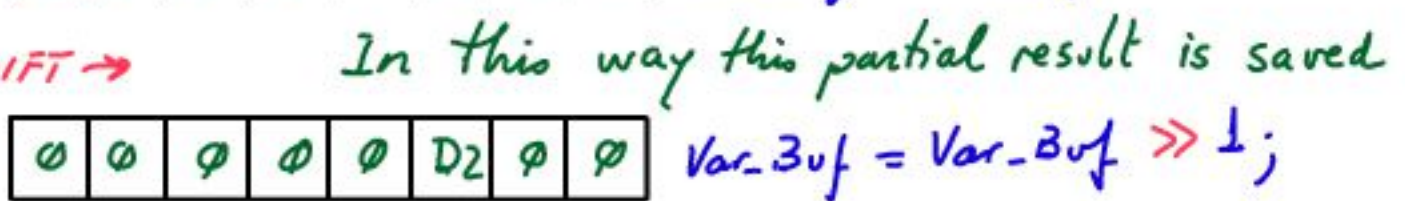
(Previously, RA5, RB3 and RB4 are configured as inputs in the `init_system()` function)



AND



SHIFT →



AND



SHIFT →



OR

