



Ask an Expert!

Intel[®] Quartus[®] Prime Design Software for FPGA Development

Join us to get your questions answered by an
Intel expert!

Register now →



Session 1: Tuesday, June 22, 2021
For the Americas and Europe
8am-9am PDT/15:00-16:00 GMT

Session 2: Wednesday, June 23, 2021 For Asia-Pacific and India 9:30am-10:30am, India Standard Time

Register now →

Ask an Expert!

Francesc J.,

This is your chance to come ask an expert about the Intel® Quartus® Prime Design Software Tools for FPGA development. This is an **interactive session** where you can come and **get answers to questions**, or **interact with other like-minded designers** who are using the Intel Quartus Prime Software tools for FPGA Development.

It doesn't matter what level you're at! If you're new to FPGA development and the Intel Quartus Prime Design Software, come and find out more. If you've been developing FPGA solutions for a while, come ask some of those detailed questions you've been wanting answered. This is an opportunity to explore the Intel Quartus Prime Design Software Tools together.

Note: The session will be conducted in English.

About the Intel® Quartus® Prime Design Software

The Intel Quartus Prime Software is the premier tool suite for doing FPGA development with Intel FPGAs.

The Intel® Quartus® Prime Software GUI supports easy design entry, fast design processing, straightforward device programming, and integration with other industry standard EDA tools. The user interface makes it easy for you to focus on your design— not on the design tool. The modular Compiler

streamlines the FPGA development process, and ensures the highest performance for the least effort.

The Intel Quartus Prime Software offers flexible design methodologies, advanced synthesis, and supports the latest Intel FPGA architectures and hierarchical design flows. The Compiler provides powerful and customizable design processing to achieve the best possible design implementation in silicon. The following features are unique to the Intel Quartus Prime Pro Edition Software:

- Hyper-Aware Design Flow—use Hyper-Retiming and Fast Forward compilation to reach the highest performance in Intel Agilex™ and Intel Stratix® 10 devices.
- Intel Quartus Prime Pro Edition synthesis—integrates new, stricter language parser supporting all major IEEE RTL languages, with enhanced algorithms, and parallel synthesis capabilities. Added support for SystemVerilog 2009.
- Hierarchical project structure—preserve individual post-synthesis, post-placement, and post-place and route results for each design instance. Allows optimization without impacting other partition placement or routing.
- Incremental Fitter Optimizations—run and optimize Fitter stages incrementally. Each Fitter stage generates detailed reports.
- Faster, more accurate I/O placement—plan interface I/O in Interface Planner.
- Platform Designer—builds on the system design and custom IP integration capabilities of Platform Designer. Platform Designer in Intel Quartus Prime Pro Edition introduces hierarchical isolation between system interconnect and IP components.
- Partial Reconfiguration—reconfigure a portion of the FPGA, while the remaining FPGA continues to function.
- Block-Based Design Flows—preserve and reuse design blocks at various stages of compilation.

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Meet Your Expert

Steven Elzinga has been in the Intel FPGA Training group for nearly 4 years. Prior to that, he designed video processing cores in FPGAs and got his start in FPGAs as an applications engineer. His current primary focus is timing closure but teaches on a variety of classes including HLS, timing analysis, and introductory courses. He is also a frequent contributor to the Intel community forums.

[Catalog of Training from Steven's Group](#)



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This was sent to francesc.j.robert@upc.edu because you are subscribed to **Webinars**. To view and manage your marketing-related email preferences with Intel, please click [here](#).

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